

L Number	Hits	Search Text	DB	Time stamp
-	427	((349/139).CCLS.)	USPAT	2002/10/23 12:00
-	9	((349/139).CCLS.) and (driv\$3 adj2 transistor)	USPAT	2002/10/23 12:30
-	161	((349/139).CCLS.) and (amorphous adj2 silicon)	USPAT	2002/10/23 12:09
-	3	((349/139).CCLS.) and (amorphous adj2 silicon)) and (poly adj2 silicon)	USPAT	2002/10/23 12:15
-	0	((349/139).CCLS.) and (amorphous adj2 silicon)) and (multi adj2 channel)	USPAT	2002/10/23 12:15
-	0	((349/139).CCLS.) and (amorphous adj2 silicon)) and (plural adj2 channel)	USPAT	2002/10/23 12:17
-	6	((349/139).CCLS.) and (gate adj2 interconnec\$4)	USPAT	2002/10/23 12:26
-	7	((349/139).CCLS.) and (laser adj2 irradiation)	USPAT	2002/10/23 12:27
-	3	((349/139).CCLS.) and (driv\$3 adj2 transistor)) and (gate adj2 line)	USPAT	2002/10/23 15:07
-	0	((349/139).CCLS.) and (driv\$3 adj2 transistor)) and (defective adj2 area)	USPAT	2002/10/23 12:39
-	1	((349/139).CCLS.) and (driv\$3 adj2 transistor)) and (malfunction)	USPAT	2002/10/23 12:39
-	57772	lcd	EPO; JPO; DERWENT	2002/10/23 15:07
-	3836	lcd and tft	EPO; JPO; DERWENT	2002/10/23 15:07
-	28	(lcd and tft) and (top adj2 gate)	EPO; JPO; DERWENT	2002/10/23 15:07
-	0	((lcd and tft) and (top adj2 gate)) and malfunction	EPO; JPO; DERWENT	2002/10/23 15:08
-	0	((lcd and tft) and (top adj2 gate)) and (drive adj2 transistor)	EPO; JPO; DERWENT	2002/10/23 15:08
-	16	(lcd and tft) and (drive adj2 transistor)	EPO; JPO; DERWENT	2002/10/23 15:08